

Electronic Acknowledgement Receipt

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Application Number:	10807478
Confirmation Number:	3056
Title of Invention:	Method of checking the layout versus the schematic of multi-fingered MOS transistor layouts using a sub-circuit based extraction
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Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment After Final	FIS920040073US1AMENDMENT.pdf	422013	no	12

Warnings:**Information:**

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.